



# Turbo IP Core Release Notes



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## 1 Turbo IP Core Release Notes

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If a release note is not available for a specific IP core version, the IP core has no changes in that version. Information on the latest update releases is in the *Intel Quartus Prime Design Suite Update Release Notes*.

### Related Links

[Intel Quartus Prime Design Suite Update Release Notes](#)

### 1.1 Turbo IP Core v17.1

**Table 1. v17.1 November 2017**

Description	Impact
Added support for Intel® Cyclone® 10 devices	-
Added support for Intel Stratix® 10 devices	-

### Related Links

- [Turbo IP Core User Guide](#)
- [Introduction to Altera IP Cores](#)
- [Errata for Turbo IP core in the Knowledge Base](#)

### 1.2 Turbo IP Core v15.1

**Table 2. v15.1 November 2015**

Description	Impact
First release.	-

### Related Links

- [Turbo IP Core User Guide](#)
- [Introduction to Altera IP Cores](#)
- [Errata for Turbo IP core in the Knowledge Base](#)

### 1.3 Turbo IP Core v16.1

**Table 3. v16.1 November 2016**

Description	Impact
<ul style="list-style-type: none"> <li>• Added 16 and 32 to <b>Number of processors</b> parameter. Output bit width varies depending on <b>Number of processors</b></li> <li>• Added <b>Number of LLRs per input</b> parameter</li> </ul>	-

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**Related Links**

- [Turbo IP Core User Guide](#)
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- [Errata for Turbo IP core in the Knowledge Base](#)