



CUSTOMER ADVISORY

ADV1906

Stratix[®]10 FPGA Package Outline Drawings Now Available

Change Description

Intel Programmable Solutions Group (“Intel PSG”, formerly Altera) is notifying customers that the Package Outline Drawings (POD) for Stratix[®]10 are now available on the Intel website at the following link:

https://www.intel.com/content/www/us/en/programmable/support/literature/lit-index/lit-pkg/package.html?family=Stratix_10

Recommended Actions

Customers are advised to take note of the Stratix 10 PODs and compare to any previously-received preliminary package drawings.

Products Affected

All Intel Stratix 10 FPGA devices.

The list of affected OPNs can be downloaded in Excel form:

<https://www.intel.com/content/dam/www/programmable/us/en/pdfs/literature/pcn/adv1906-opn-list.xlsx>

Change Implementation

Table 1

<i>Milestone</i>	<i>Date of Availability</i>
Date of POD Availability	Now

Contact

For more information, please contact your Sales representative or submit a Service Request at the [My Intel](#) support page.

Customer Notifications Subscription

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If you would like to receive customer notifications by email, please subscribe to our customer notification mailing list at:

<https://www.intel.com/content/www/us/en/programmable/my-intel/mal-emailsub/technical-updates.html>

Revision History

Date	Rev	Description
04/19/2019	1.0.0	Initial Release

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